

Subclass	ISSUE CLASSIFICATION
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IJ,	S.	UTILITY	Patent	Application
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Masayuki Ishiwa Kenji Iizuka Takeshi Nishimura	PLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS			
Masayuki Ishiwa Kenji Iizuka Takashi Nishimura	22/(/369)			SUBCLASS	ART UNIT	EXAMINER	
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Multilayer circuit board	Multilayer	Circuit b	sama ma an daf		,		

TITLE

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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G	
The term of this patent subsequent to(date)				NOTICE OF AL	LOWANCE MAILED	
has been disclaimed.	(Assistant i	=xaminer)	(Date)		***	
The term of this patent shall not extend beyond the expiration date			1-310)		<u> </u>	
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ARNING: ne information disclosed herein may be restrict pagession outside the U.S. Patent & Trademar	cted. Unauthorized dis k Office is restricted to	sclosure may be pro		ited States Code Title 35,	Sections 122, 181 and 368.	
Ossession outside the U.S. Patent & Trademai rm PTO-436A w. 8/99)	cted. Unauthorized dis k Office is restricted to		phibited by the Unites and contractor		Sections 122, 181 and 3	